

Summary

Looking for an Internship and Full-Time Job Opportunity.

Education

Illinois Institute of Technology, Chicago, IL Graduation: Dec 2024

Master's in Electrical and Computer Engineering

Specialization: VLSI and Microelectronics – 1) Advance VLSI Design, 2) CAD using VLSI Design, 3) Digital SoC Design

SRM University, Andhra Pradesh

Graduation: May 2022

Bachelor's in Electronics and Communication Engineering

Specialization: VLSI, Digital and Analog Electronics

Skills

Coding and Conceptual: Python, C, TCL/TK, Verilog, Hspice •Technologies: (specializing in Microsoft Office and Google programs), Cadence, Xilinx(VIVADO), EDA tools •Interpersonal: Research, Motivational, Organizational, Volunteer.

Industrial Experiences

VLSI Design | Junior RTL Design Engineer June2021 – Aug 2021

VLSIUVM TECHNOLOGIES (OPC) PRIVATE LIMITED Nagpur, India

- I was contributing to the Verilog code generator project by creating synthesizable RTL designs code, developing test plans, and performing simulation checks, and timing checking. By utilized The EDA tools, Model sim Altera, Xilinx.

VLSI | Student Intern Jan 2021 – Mar 2021

Electronics Corporation of India Limited (ECIL) Hyderabad, India

- During this internship, I adopted the ideas of Verilog code generation with concepts of digital electronics, and the implementation of an 8-bit ALU utilising the Verilog language. The functional verification of design testbench is employed, and all functional operations are checked using the tools Xilinx (ISE), FPGA.

Telemetry | Student Intern Dec 2019 – Jan 2020

Defence Research & Development Laboratory (DRDL) Hyderabad, India

- Understanding the technology used in missiles and the Telemetry concepts on which the basics of microwave applications.
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Projects

Performance Evaluation of 6T and 8T SRAM cell using ASAP7 Technology IIT-Chicago, IL

- In this project 6T and 8T SRAM is designed in ASAP7 and parameters analysed using SPICE, Cscope. Also compared the layout vs schematic (DRC, LVS, PEX) synthesis • Tools / Technologies: ASPA7(7nm), Cadence, SPICE.

Design of a Unified XOR Ring Oscillator PUF-TRNG Circuit in 45nm CMOS Technology SRM University, AP

- Various architectures and designs have been explored in earlier research work, thus in this current study we investigate the XOR function form of unified XOR-RO-PUF TRNG circuit, the calibration and parameters analysis are taken while comparing with prior RO-PUF-TRNG papers. • Tools / Technologies: Cadence (VM ware), CMOS, FPGA • Presented in Research Day 2022 SRM University, AP • Published in IEEE.

Performance Evaluation of 6T SRAM cell using 90 nm Technology SRM University, AP

- In this project performance evaluation of 6tsram cell topology and analysing the different parameters using 90nm node. • Tools / Technologies: Cadence (VM ware), CMOS • Presented in Research Day 2021 SRM University, AP • Published in IJERT.

Implementation of the Communication Protocol SPI by the HDL-Verilog Language. SRM University, AP

- In this project design and development of SPI by create the single input Master and single input Slave. For Finding the Full duplex Communication • Tools / Technologies: : EDA tools, Model sim Altera, XILINX(VIVADO)• Presented in Research Day 2020 SRM University, AP • Submitted in IEEE.

Image Enhancement Based on Hardware Descriptive Language. SRM University, AP

- The project deals with image enhancement techniques. The main theme is the enhancement of image to find A) Threshold Operation B) Invert Operation C) Brightness •Tools / Technologies: MATLAB, XILINX(VIVADO)•Presented in Research Day 2020 SRM University, AP• Published in IRPH.

ECG Monitoring System Using Arduino Uno and AD8232 Sensor SRM University, AP

- In this project, we built a 3-lead ECG machine using an Arduino, an AD8232 signal processor, and a Bluetooth module. It is designed and implemented for home use using remote medical-related technology that monitors ECG signals and displays heart rate in real time •Tools / Technologies: Arduino, AD8232, C, ECG• Submitted in IEEE
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Extracurricular And Leadership

Tele – Hawks Graduate Student Ambassador Nov 2022

Graduate Admissions Department Chicago, IL

- Lead several initiatives to make knowledge available to students from all around the world. Develop relationships with potential students via phone, email, text, social media, and in-person visits. Provided training and shadowing sessions to new team members.

Sales Consultant Aug 2021 – Mar 2022

Herbalife Nutrition India

- Working as a sales consultant taught me how to communicate with people, and accounting was interesting. It was most rewarding when I could show someone else how to enhance their health and money. Customers were shown how to use items and manage their money.

Student Chapter Aug 2019 – May 2021

SRM Student council, E-CELL, E-Sports, Meditation and Yoga Club, Literary Club in SRM University, AP Amaravati, India

- As a member of the student body, I engage with many students, answering their questions and arranging cultural events and technical courses. I also implement student suggestions to build a more ecologically friendly environment.

Young Conversionist Aug 2021 – Sep 2021

WWF (worldwide fund for nature) India

- Team member and event organizer and collaborate with well-known people in comparable zones and disciplines such as Linked. In, Social Forums.
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Perks

Winner of Tech Fest, at Sir C.R Reddy College of Engineering • (Hayakawa) Japanese Language Level – N5 certified • Gold Medal (Top 50 University Admission) • Research Day SRM University, AP 2022, 2021).